Note: This datasheet may be out of date.

Please download the latest datasheet of CSTCR6M00G15L99-R0 from the official website of Murata

Manufacturing Co., Ltd.

http://www.murata.com/en-us/products/productdetail?partno=CSTCR6M00G15L99-R0

CSTCR6M00G15L99-R0



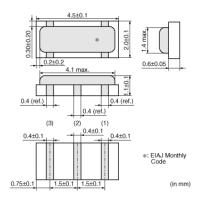






Appearance & Shape







Features

MURATA's frequency adjustment and packaging technology expertise has enabled the development of the chip CERALOCK(R) with built-in load capacitors. High-density mounting is made possible by the small package and the elimination of the need for an external load capacitor.

Features

- 1. Oscillation circuits do not require external load capacitors.
- 2. Available in a wide frequency range.
- 3. Extremely small and have a low profile.
- 4. No adjustment is necessary for oscillation circuits.

Applications

Other Usage

Consumer/Industrial



Packaging Information

Packaging	Specifications	Minimum Order Quantity
-R0	180mm Embossed Tape	3000

1 of 3

Attention

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2. This datasheet has only typical specifications because there is no space for detailed specifications.

Therefore, please review our product specifications or consult the approval sheet for product specifications before ordering.





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CSTCR6M00G15L99-R0



Shape	SMD
Frequency	6.000MHz
Frequency Tolerance	±0.1%
Operating Temperature Range	0°C to 70°C
Frequency Shift by Temperature	±0.08%
Frequency Aging	±0.07%
Wash	Not available
Resonant Impedance	50Ω
Built-in Load Capacitance	39pF

2 of 3

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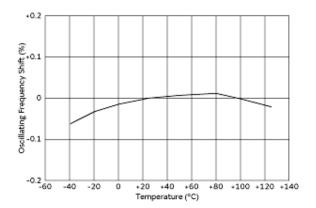
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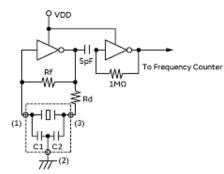
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CSTCR6M00G15L99-R0







Regarding CERALOCK® with built-in capacitors, the terminal (1) and (3) are interchangeable, but if connection is interchanged, the specification of initial tolerance frequency may not be guaranteed. *Please see dimensions about terminal numbers.

Temperature Characteristics

Measurement Circuit

3 of 3

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