



PS2815-1, PS2815-4

Data Sheet R08DS0103EJ0501 Rev.5.01 Jan 13, 2015

LOW (AC) INPUT CURRENT, HIGH CTR 4, 16-PIN SSOP PHOTOCOUPLER

DESCRIPTION

The PS2815-1 and PS2815-4 are optically coupled isolators containing GaAs light emitting diodes and an NPN silicon phototransistor in a plastic SSOP for high density applications.

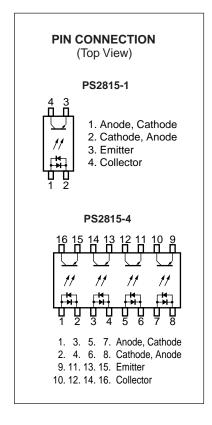
The package is a Shrink SOP (Small Outline Package) type for high density mounting applications.

FEATURES

- AC input response
- High current transfer ratio (CTR = 200% TYP. @ $I_F = \pm 1$ mA)
- High isolation voltage (BV = 2 500 Vr.m.s.)
- Small and thin package (4, 16-pin SSOP, Pin pitch 1.27 mm)
- Ordering number of taping product: PS2815-1-F3: 3 500 pcs/reel
 : PS2815-4-F3: 2 500 pcs/reel
- Pb-Free product
- · Safety standards
 - UL approved: No. E72422
 - CSA approved: No. CA 101391 (CA5A, CAN/CSA-C22.2 60065, 60950)
 - DIN EN 60747-5-5 (VDE 0884-5) approved (Option)

APPLICATIONS

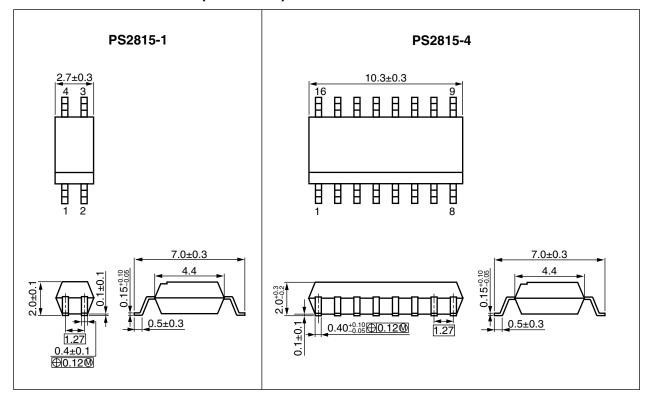
- Programmable logic controllers
- Modem/FAX



The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

PACKAGE DIMENSIONS (UNIT: mm)

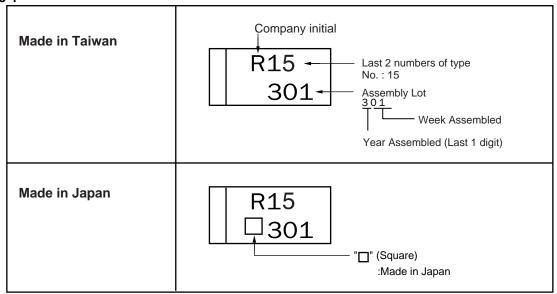


PHOTOCOUPLER CONSTRUCTION

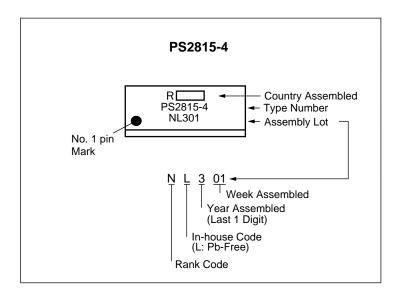
Parameter	Unit (MIN.)
Air Distance	4.5 mm
Creepage Distance	4.5 mm
Isolation Thickness	0.1 mm

MARKING EXAMPLE

PS2815-1



PS2815-4



ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standards Approval	Application Part Number*1
PS2815-1-F3	PS2815-1-F3-A	Pb-Free	Embossed Tape 3 500 pcs/reel	Standard products (UL, CSA approved)	PS2815-1
PS2815-1-V-F3	PS2815-1-V-F3-A		Embossed Tape 3 500 pcs/reel	DIN EN 60747-5-5 (VDE 0884-5) Approved (Option)	
PS2815-4-F3	PS2815-4-F3-A		Embossed Tape 2 500 pcs/reel	Standard products (UL, CSA approved)	PS2815-4
PS2815-4-V-F3	PS2815-4-V-F3-A	Embossed Tape 2 500 pcs/reel		DIN EN 60747-5-5 (VDE 0884-5) Approved (Option)	

Note: *1. For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings		Unit
			PS2815-1	PS2815-4	
Diode	Forward Current (DC)	I _F	±50		mA/ch
	Power Dissipation Derating	⊿P _D /°C	0.6	0.7	mW/°C
	Power Dissipation	P _D	60	70	mW/ch
	Peak Forward Current ^{*1}	I _{FP}	±′	1.0	A/ch
Transistor	Collector to Emitter Voltage	V _{CEO}	40		V
	Emitter to Collector Voltage	V _{ECO}	5		V
	Collector Current	Ic	40		mA/ch
	Power Dissipation Derating	⊿P _c /°C	1.2		mW/°C
	Power Dissipation	Pc	120		mW/ch
Isolation Vo	oltage *2	BV	25	500	Vr.m.s.
Operating Ambient Temperature		T _A	-55 to +100		°C
Storage Te	Storage Temperature		−55 to +150		°C

Notes: * 1. PW = 100 μ s, Duty Cycle = 1%

 $^{\star}2$. AC voltage for 1 minute at T_A = 25°C, RH = 60% between input and output.

Pins 1-2 shorted together, 3-4 shorted together (PS2815-1).

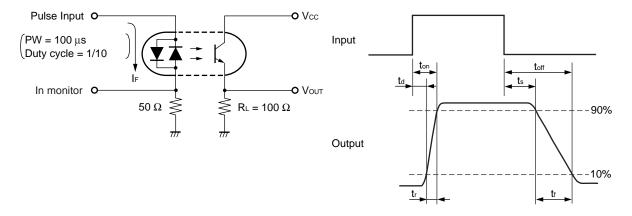
Pins 1-8 shorted together, 9-16 shorted together (PS2815-4).



ELECTRICAL CHARACTERISTICS (TA = 25°C)

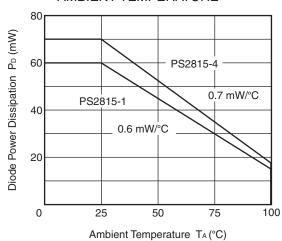
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	V _F	$I_F = \pm 5 \text{ mA}$		1.15	1.4	V
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz		30		pF
Transistor	Collector to Emitter Dark Current	I _{CEO}	I _F = 0 mA, V _{CE} = 40 V			100	nA
Coupled	Current Transfer Ratio (I _C /I _F) *1	CTR	$I_F = \pm 1 \text{ mA}, V_{CE} = 5 \text{ V}$	100	200	400	%
	Collector Saturation Voltage	V _{CE (sat)}	$I_F = \pm 1 \text{ mA}, I_C = 0.2 \text{ mA}$			0.3	V
	Isolation Resistance	R _{I-O}	$V_{I-O} = 1 \text{ kV}_{DC}$	10 ¹¹			Ω
	Isolation Capacitance	C _{I-O}	V = 0 V, f = 1 MHz		0.4		pF
	Rise Time *1	t _r	$V_{CC} = 5 \text{ V}, I_{C} = 2 \text{ mA}, R_{L} = 100 \Omega$		4		μS
	Fall Time *1	t _f			5		
	Turn-on Time*1	t _{on}			7		
	Turn-off Time*1	t _{off}			5		

Notes: *1. Test circuit for switching time

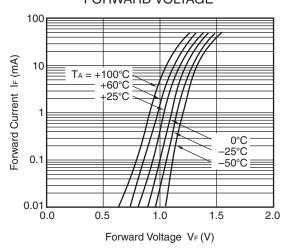


TYPICAL CHARACTERISTICS (T_A = 25°C, unless otherwise specified)

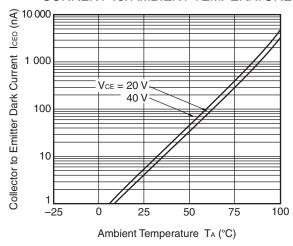
DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



FORWARD CURRENT vs. FORWARD VOLTAGE

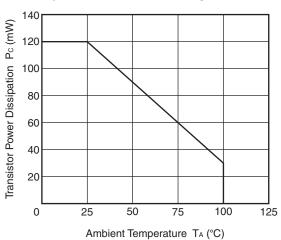


COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE

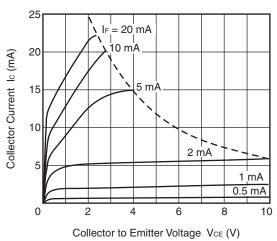


Remark The graphs indicate nominal characteristics.

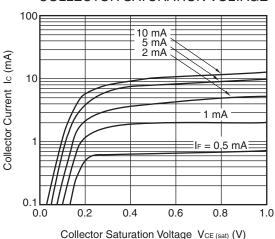
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



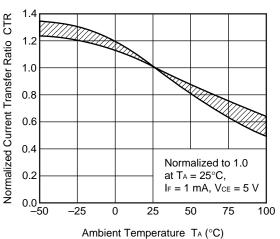
COLLECTOR CURRENT vs. **COLLECTOR TO EMITTER VOLTAGE**



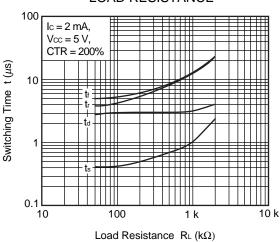
COLLECTOR CURRENT vs. **COLLECTOR SATURATION VOLTAGE**



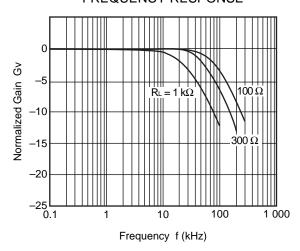
NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



SWITCHING TIME vs. LOAD RESISTANCE

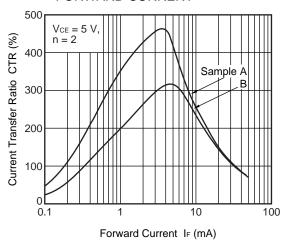


FREQUENCY RESPONSE

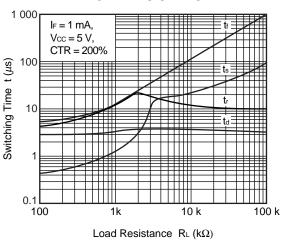


Remark The graphs indicate nominal characteristics.

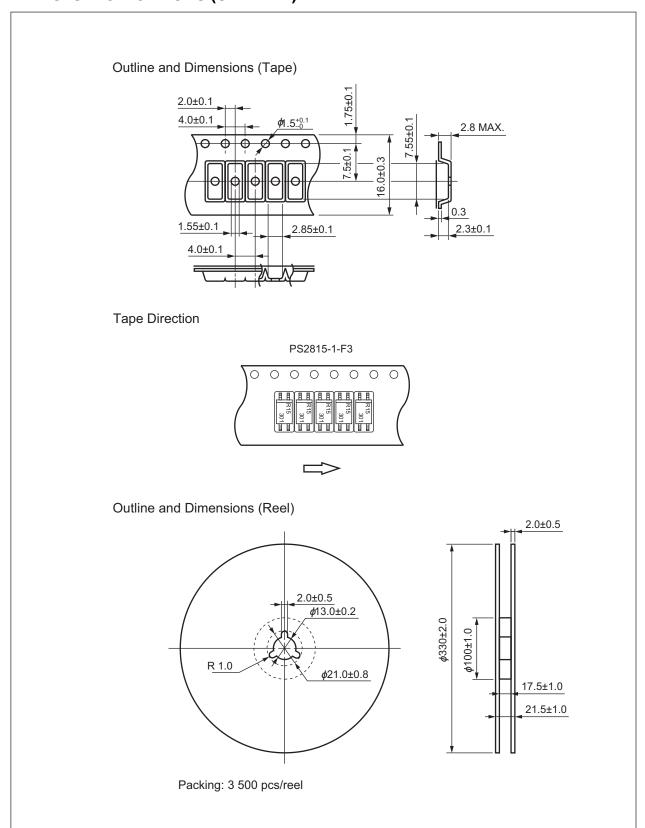
CURRENT TRANSFER RATIO vs. FORWARD CURRENT

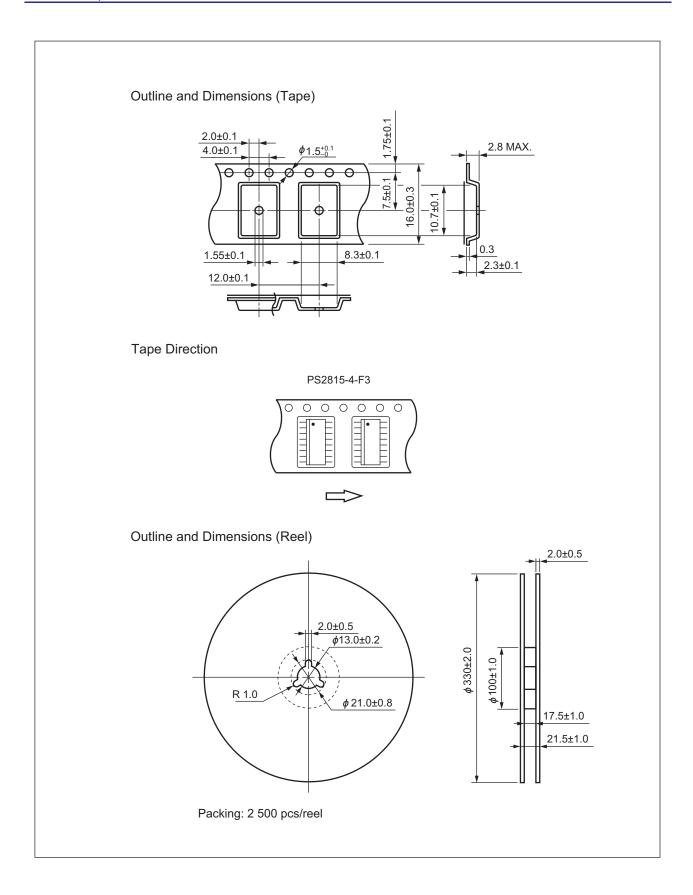


SWITCHING TIME vs. LOAD RESISTANCE

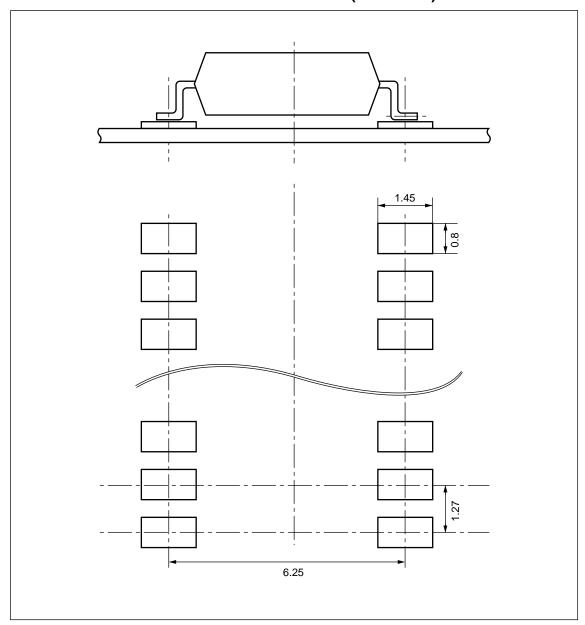


TAPING SPECIFICATIONS (UNIT: mm)





RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



Remark All dimensions in this figure must be evaluated before use.

NOTES ON HANDLING

- 1. Recommended soldering conditions
 - (1) Infrared reflow soldering

Peak reflow temperature 260°C or below (package surface temperature)

Time of peak reflow temperature 10 seconds or less Time of temperature higher than 220°C 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s Three

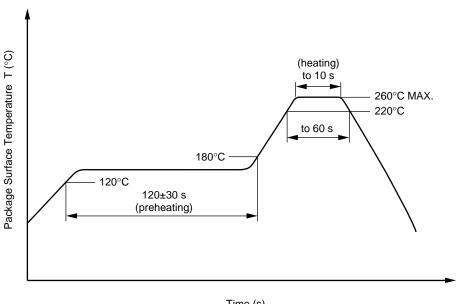
Number of reflows

Flux

Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is

recommended.)

Recommended Temperature Profile of Infrared Reflow



Time (s)

(2) Wave soldering

Temperature 260°C or below (molten solder temperature)

Time 10 seconds or less

Preheating conditions 120°C or below (package surface temperature)

Number of times One (Allowed to be dipped in solder including plastic mold portion.)

Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below

Time (each pins) 3 seconds or less

Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(4) Cautions

 Fluxes Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

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2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collectoremitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler
 Check the setting values before use, since the forward current conditions at CTR measurement differ according to
 product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

USAGE CAUTIONS

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.



SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Spec.	Unit
Climatic test class (IEC 60068-1/DIN EN 60068-1)		55/100/21	
Dielectric strength maximum operating isolation voltage $Test\ voltage\ (partial\ discharge\ test,\ procedure\ a\ for\ type\ test\ and\ random\ test)$ $U_{pr}=1.6\times U_{IORM},\ P_d<5\ pC$	Uiorm Upr	705 1 128	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{IORM}, P_d < 5 \; pC$	Upr	1 322	V_{peak}
Highest permissible overvoltage	Utr	6 000	V _{peak}
Degree of pollution (DIN EN 60664-1 VDE 0110 Part 1)		2	
Comparative tracking index (IEC 60112/DIN EN 60112 (VDE 0303 Part 11))	СТІ	175	
Material group (DIN EN 60664-1 VDE 0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +150	°C
Operating temperature range	TA	-55 to +100	°C
Isolation resistance, minimum value VIO = 500 V dc at TA = 25°C VIO = 500 V dc at TA MAX. at least 100°C	Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation) Isolation resistance	Tsi Isi Psi	150 300 500	°C mA mW
Vio = 500 V dc at T _A = Tsi	Ris MIN.	10 ⁹	Ω

Caution GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
- Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

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