





MMBT5401

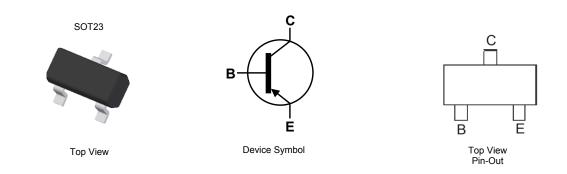
150V PNP SMALL SIGNAL TRANSISTOR IN SOT23

Features

- Epitaxial Planar Die Construction
- Complementary NPN Type MMBT5551
- Ideal for Low Power Amplification and Switching
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP capable (Note 4)

Mechanical Data

- Case: SOT23
- Case material: molded plastic, "Green" molding compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208⁽³⁾
- Weight: 0.008 grams (Approximate)



Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMBT5401-7-F	AEC-Q101	K4M	7	8	3,000
MMBT5401-13-F	AEC-Q101	K4M	13	8	10,000
MMBT5401Q-7-F	Automotive	K4M	7	8	3,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

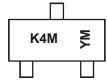
 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



K4M = Product Type Marking Code YM = Date Code Marking Y = Year (ex: N = 2002)

M = Month (ex: 9 = September)

Notes:

Build Court	,																	
Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
Code	J	К	L	М	Ν	Р	R	s	Т	U	V	W	Х	Y	Z	Α	В	С
Month	Jan	1	Feb	Ma	r	Apr	May	/	Jun	Jul		Aug	Sep		Oct	Nov	,	Dec
Code	1		2	3		4	5		6	7		8	9		0	N		D



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-160	V
Collector-Emitter Voltage	V _{CEO}	-150	V
Emitter-Base Voltage	V _{EBO}	-5.0	V
Collector Current	Ι _C	-600	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 6)	Р	310	mW
Fower Dissipation	(Note 7)	P _D	350	11100
Thermal Desistance Junction to Ambient	(Note 6)	P	403	0000
Thermal Resistance, Junction to Ambient	(Note 7)	R _{eja}	357	°C/W
Thermal Resistance, Junction to Leads	R _{θJL}	350	°C/W	
Operating and Storage Temperature Range		T _J ,T _{STG}	-55 to +150	°C

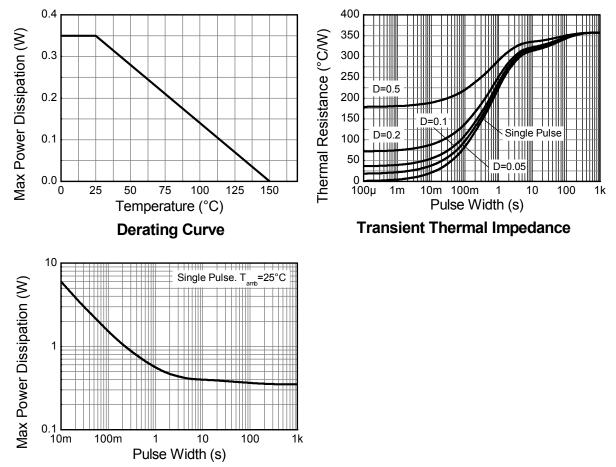
ESD Ratings (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 6. For a device mounted on minimum recommended pad layout 1oz copper that is on a single-sided FR4 PCB; device is measured under still air For a device monted on minimum recommended paragout 102 copper that is a conditions whilst operating in a steady-state.
Same as note (6), except the device is mounted on 15 mm x 15mm 1oz copper.
Thermal resistance from junction to solder-point (at the end of the leads).
Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information



Pulse Power Dissipation



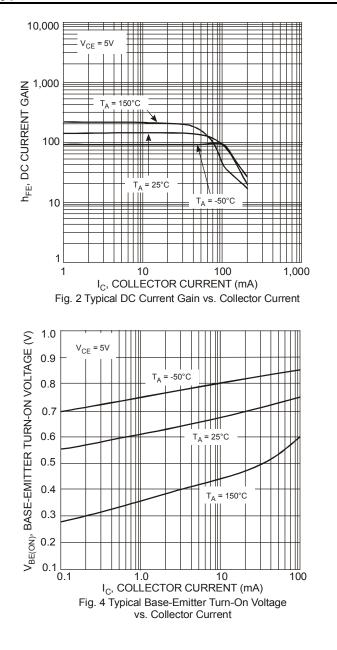
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

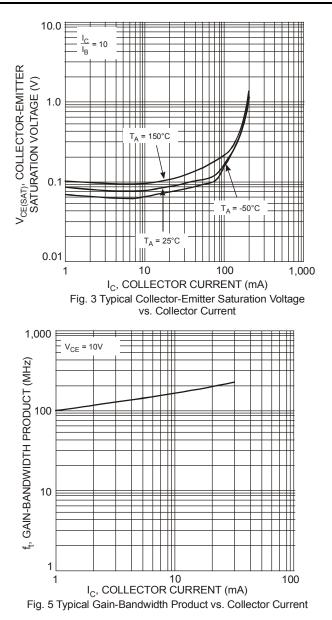
Characteristic	Symphol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 10)	Symbol	Min	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-160	_	V	$I_{\rm C} = -100\mu A, I_{\rm E} = 0$
Collector-Emitter Breakdown Voltage	BVCEO	-150	_	V	$I_{\rm C} = -1.0 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	BV _{EBO}	-5.0	_	V	$I_{\rm E} = -10\mu A, I_{\rm C} = 0$
Collector Cutoff Current	Ісво		-50	nA	$V_{CB} = -120V, I_E = 0$
	ICBO		-50	μA	V _{CB} = -120V, I _E = 0, T _A = +100°C
Emitter Cutoff Current	I _{EBO}	_	-50	nA	$V_{EB} = -4.0V, I_C = 0$
ON CHARACTERISTICS (Note 10)			1	r	
		50			$I_{C} = -1.0 \text{mA}, V_{CE} = -5.0 \text{V}$
DC Current Gain	h _{FE}	60	240	—	$I_{C} = -10 \text{mA}, V_{CE} = -5.0 \text{V}$
		50			$I_{C} = -50 \text{mA}, V_{CE} = -5.0 \text{V}$
Collector-Emitter Saturation Voltage	M	—	-0.2	V	I _C = -10mA, I _B = -1.0mA
	V _{CE(SAT)}		-0.5	v	I _C = -50mA, I _B = -5.0mA
Page Emitter Seturation Voltage	<i>\</i> /	_	-1.0	V	I _C = -10mA, I _B = -1.0mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}				I _C = -50mA, I _B = -5.0mA
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C _{obo}		6.0	pF	V _{CB} = -10V, f = 1.0MHz, I _E = 0
Small Signal Current Gain	h _{fe}	40	200		V _{CE} = -10V, I _C = -1.0mA,
	Tite	-10	200		f = 1.0kHz
Current Gain-Bandwidth Product	f⊤	100	300	MHz	$V_{CE} = -10V, I_C = -10mA,$
	.1				f = 100MHz
Noise Figure	NF		8.0	dB	V_{CE} = -5.0V, I_{C} = -200 μ A,
			5.0	30	R _S = 10Ω, f = 1.0kHz

Notes: 10. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.



Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

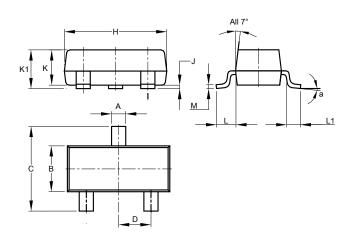






Package Outline Dimensions

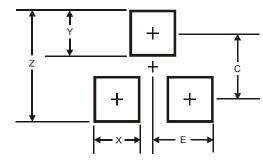
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23								
Dim	Min	Max	Тур						
Α	0.37	0.51	0.40						
В	1.20	1.40	1.30						
С	2.30	2.50	2.40						
D	0.89	1.03	0.915						
F	0.45	0.60	0.535						
G	1.78	2.05	1.83						
Н	2.80	3.00	2.90						
J	0.013	0.10	0.05						
κ	0.890	1.00	0.975						
K1	0.903	1.10	1.025						
L	0.45	0.61	0.55						
L1	0.25	0.55	0.40						
М	0.085	0.150	0.110						
а	8°								
All	Dimens	ions in	mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.



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