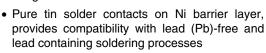


Long Side Termination Thick Film Chip Resistors



FEATURES

- · Enhanced power rating
- · Long side terminations





- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition
- AEC-Q200 qualified

STANDARD ELECTRICAL SPECIFICATIONS											
	SIZE		25-06198 RATED	LIMITING	TEMPEDATURE		550054105	SERIES			
MODEL	INCH	METRIC	DISSIPATION P ₇₀ W	ELEMENT VOLTAGE U _{max.} AC/DC V	TEMPERATURE COEFFICIENT ppm/K		RESISTANCE RANGE Ω				
RCL0612 e3	0612	RR 1632M	0.5	75	± 100	± 1	1R0 to 1M	E24; E96			
HOL0012 63	0012	nn 1032W	0.5	0.5 75 ± 200		± 5	THO TO TIVE	E24			
RCL1218 e3	1218	RR 3246M	1.0	200	± 100	± 1	1R0 to 2.2M	E24; E96			
NOL1210 ES	1210	nn 3240ivi	1.0	200	± 200	± 5	1 NO 10 2.2IVI	E24			
RCL1225 e3	1225	RR 3263M	DD 2062M	DD 2060M	DD 2062M	2.0 (1)	200	± 100	± 1	1R0 to 1M	E24; E96
HOL1220 60	1223	nn 3203IVI	2.0 (1)	200	± 200	± 5	I HO TO TIVI	E24			

Notes

- These resistors do not feature a limited lifetime when operated within the permissible limits. However, resistance value drift increasing over operating time may result in exceeding a limit acceptable to the specific application, thereby establishing a functional lifetime.
- Marking: See datasheet "Surface Mount Resistor Marking" (document number 20020).
- Power rating depends on the max. temperature at the solder point, the component placement density and the substrate material.
- (1) Specified power rating requires dedicated mounting conditions to achieve the required thermal resistance.

TECHNICAL SPECIFICATIONS								
DESCRIPTION	UNIT	RCL0612	RCL1218	RCL1225				
Rated Dissipation P ₇₀ (2)	W	0.5	1.0	2.0 (3)				
Limiting Element Voltage U _{max.} AC/DC	V	75	200	200				
Insulation Voltage U _{ins} (1 min)	V	> 100	> 300	> 300				
Insulation Resistance	Ω	> 10 ⁹						
Category Temperature Range	°C	- 55 to + 155						
Weight	mg	11	29.5	55				

Notes

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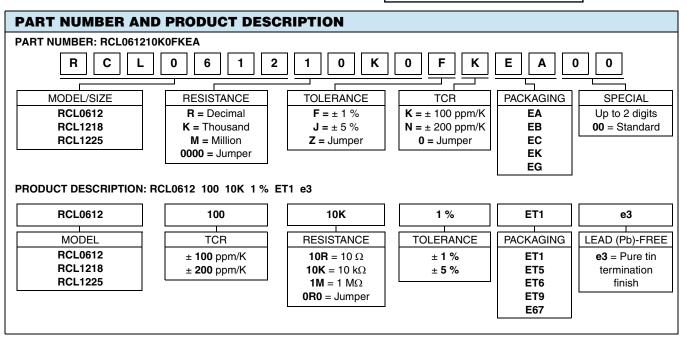
⁽²⁾ The power dissipation on the resistors generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature of 155 °C is not exceeded.

⁽³⁾ Specified power rating requires dedicated mounting conditions to achieve the required thermal resistance.

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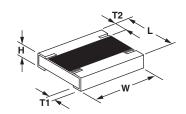


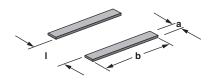
RCL12180000Z0EK



PACKA	PACKAGING								
MODEL	UNIT	A	PAPER TAPE ON L CC. TO IEC 60286-3		BLISTER TAPE ON REEL ACC. TO IEC 60286-3, TYPE II				
		QUANTITY	PART NUMBER	PRODUCT DESC.	QUANTITY	PART NUMBER	PRODUCT DESC.		
	180 mm/7"	5000	EA	ET1					
RCL0612	285 mm/11.25"	10 000	EB	ET5					
	330 mm/13"	20 000	EC	ET6					
RCL1218	180 mm/7"				4000	EK	ET9		
RCL1225	180 mm/7"				2000	EG	E67		

DIMENSIONS in millimeters





	IZE	DIMENSIONS						SOLDER PAD DIMENSIONS				
3	OIZE							REFLOW SOLDERING			WAVE SOLDERING	
INCH	METRIC	L	W	Н	T1	T2	а	b	I	а	b	I
0612	1632	1.6 ± 0.2	3.2 ± 0.2	0.55 ± 0.1	0.35 ± 0.15	0.25 ± 0.15	0.6	3.2	1.0	1.1	3.2	1.0
1218	3246	3.2 ^{+ 0.10} - 0.20	4.6 ± 0.15	0.55 ± 0.05	0.45 ± 0.2	0.4 ± 0.2	1.1	4.9	1.9	1.25	4.8	1.9
1225	3263	3.2 ± 0.2	6.3 ± 0.2	0.75 ± 0.15	0.8 ± 0.2	0.4 ± 0.2	1.9	7.6	1.2	1.9	7.6	1.2

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For technical questions, contact: thickfilmchip@vishay.com

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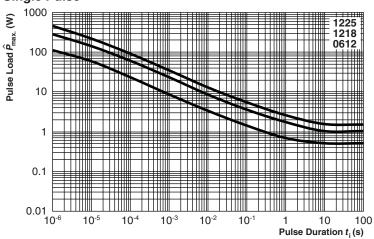
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Long Side Termination Thick Film Chip Resistors

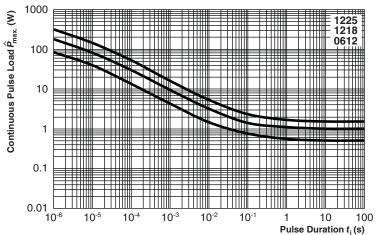
FUNCTIONAL PERFORMANCE

Single Pulse



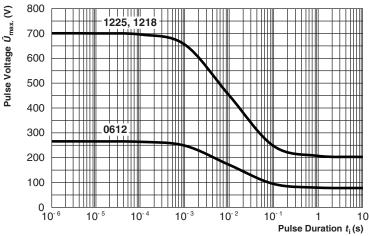
Maximum pulse load, single pulse; applicable if $\bar{P} \rightarrow 0$ and n < 1000 and $\hat{U} \leq \hat{U}_{max}$; for permissible resistance change equivalent to 8000 h operation

Continuous Pulse



Maximum pulse load, continuous pulses; applicable if $\bar{P} \leq P \left(\vartheta_{amb} \right)$ and $\hat{U} \leq \hat{U}_{max}$; for permissible resistance change equivalent to 8000 h operation

Pulse Voltage



Maximum pulse voltage, single and continuous pulses; applicable if $\hat{P} \leq \hat{P}_{\text{max}}$; for permissible resistance change equivalent to 8000 h operation

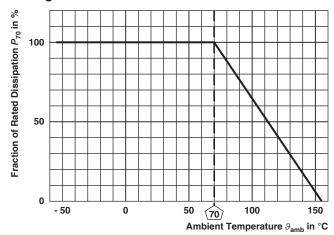
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Long Side Termination Thick Film Chip Resistors



Derating



TEST PROCEDURES AND REQUIREMENTS								
EN 60068- 60115-1 TEST		TEST	PROCEDURE -	REQUIREMENTS PERMISSIBLE CHANGE (△ <i>R</i>)				
CLAUSE	METHOD			STABILITY CLASS 2 OR BETTER				
			Stability for product types:					
			RCL e3	1 Ω to 2.2 MΩ				
4.5	-	Resistance	-	± 1 %	± 5 %			
4.7	-	Voltage proof	$U = 1.4 \times U_{ins}$; 60 s	No flashover	or breakdown			
4.13	-	Short time overload	$U = 2.5 \times \sqrt{P_{70} \times R}$ $\leq 2 \times U_{\text{max.}};$ Duration acc. to style	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)			
	58 (Td)	Solderability	Solder bath method; Sn60Pb40 non activated flux; (235 ± 5) °C (2 ± 0.2) s	Good tinning (≥ 95 % covered); no visible damage				
4.17.2 58 (Td)) Soliderability	Solder bath method; Sn96.5Ag3Cu0.5 non-activated flux; (245 ± 5) °C (3 ± 0.3) s	Good tinning (≥ 95 % covered); no visible damage				
4.8.4.2	-	Temperature coefficient	(20/- 55/20) °C and (20/125/20) °C	± 100 ppm/K	± 200 ppm/K			
4.32	21 (Uu ₃)	Shear (adhesion)	45N	No visible damage				
4.33	21 (Uu ₁) Substrate bending		Depth 2 mm; 3 times	No visible damage, no open circuit in bent posit $\pm \ (0.25 \ \% \ R + 0.05 \ \Omega)$				
4.19		Rapid change of	30 min at - 55 °C; 30 min at 125 °C					
	14 (Na)	temperature	5 cycles	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)			
			1000 cycles	± (1 % R + 0.05 Ω)	± (1 % R + 0.05 Ω)			



Long Side Termination Thick Film Chip Resistors

TEST PROCEDURES AND REQUIREMENTS							
EN 60115-1	0115-1 60068-2 TEST		PROCEDURE	REQUIRI PERMISSIBLE			
CLAUSE				STABILITY CLAS	ISS 2 OR BETTER		
			Stability for product types:				
			RCL e3	1 Ω to 2.2 MΩ			
4.23	-	Climatic sequence:	-				
4.23.2	2 (Ba)	Dry heat	125 °C; 16 h				
4.23.3	30 (Db)	Damp heat, cyclic	55 °C; ≥ 90 % RH; 24 h; 1 cycle				
4.23.4	1 (Aa)	Cold	- 55 °C; 2 h	± (1 % <i>R</i> + 0.05 Ω)	\pm (2 % R + 0.1 Ω)		
4.23.5	13 (M)	Low air pressure	1 kPa; (25 ± 10) °C; 1 h				
4.23.6	30 (Db)	Damp heat, cyclic	55 °C; ≥ 90 % RH; 24 h; 5 cycles				
4.23.7	-	DC load	$U = \sqrt{P_{70} \times R}$				
4.05.4		Endurance	$U = \sqrt{P_{70} \times R} \le U_{\text{max.}};$ 1.5 h on; 0.5 h off;				
4.25.1	-	at 70 °C	°C 70 °C; 1000 h \pm (0.5 % R + 0.05 Ω)		$\pm (2 \% R + 0.1 \Omega)$		
			70 °C; 8000 h	± (1 % R + 0.05 Ω)	± (4 % R + 0.1 Ω)		
4.18.2	58 (Td)	Resistance to soldering heat	Solder bath method (260 ± 5) °C; (10 ± 1) s	$\pm (0.25 \% R + 0.05 \Omega)$	$\pm (0.5 \% R + 0.05 \Omega)$		
4.35	-	Flamability, needle flame test	IEC 60695-11-5; 10 s	No burning	after 30 s		
4.24	78 (Cab)	Damp heat, steady state	(40 ± 2) °C; (93 ± 3) % RH; 56 days	± (1 % R	+ 0.05 Ω)		
4.25.3	-	Endurance at upper category temperature	155 °C, 1000 h	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)		
4.40	-	Electrostatic discharge (Human Body Model)	IEC 61340-3-1 3 pos. + 3 neg. discharges; ESD voltage: 1000 V	± (1 % <i>R</i> ·	+ 0.05 Ω)		
4.29	45 (XA)	Component solvent resistance	Isopropyl alcohol; 50 °C; method 2	No visible	damage		
4.30	45 (XA)	Solvent resistance of marking	Isopropyl alcohol; 50 °C; method 1, toothbrush	Marking no visible			
4.22	6 (Fc)	Vibration, endurance by sweeping	$ f = 10 \text{ Hz to } 2000 \text{ Hz}; \\ x, y, z \le 1.5 \text{ mm}; \\ A \le 200 \text{ m/s}^2; \\ 10 \text{ sweeps per axis} $	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)		
4.37	-	Periodic electric overload	$U = \sqrt{15 \times P_{70} \times R}$ $\leq 2 \times U_{\text{max.}};$ 0.1 s on; 2.5 s off; 1000 cycles	± (1 % <i>R</i> ·	+ 0.05 Ω)		
4.27	-	Single pulse high voltage overload, 10 µs/700 µs	$\hat{U} = 10 \text{ x } \sqrt{P_{70} \text{ x } R}$ $\leq 2 \text{ x } U_{\text{max}};$ 10 pulses	± (1 % R	+ 0.05 Ω)		

All tests are carried out in accordance with the following specifications:

- EN 60115-1, generic specification
- EN 140400, sectional specification
- EN 140401-802, detail specification
- IEC 60068-2-x environmental test procedures

Packaging of components is done in paper or blister tapes according to IEC 60286-3

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